



Att. Dkt. No. 029471-0194

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Takeshi INUO

Title: A RECONFIGURABLE ELECTRONIC COMPUTER,
SEMICONDUCTOR INTEGRATED CIRCUIT AND CONTROL
METHOD, PROGRAM GENERATION METHOD, AND PROGRAM
FOR CONTROLLING RECONFIGURABLE HARDWARE (as amended)

Application No.: 10/561,941

International Filing Date: 06/21/2004
371(c) Date: 2/15/2006

Examiner: Keith E. Vicary

Art Unit: 2183

Confirmation No.: 3034

AMENDMENT AND REPLY UNDER 37 CFR 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Final Office Action dated August 14, 2007, concerning the above-referenced patent application.

Amendments to the Specification begin on page 2 of this document.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this document.

Remarks/Arguments begin on page 11 of this document.

Please amend the application as follows: